

Electronic Acknowledgement Receipt

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Application Number:	10748906
International Application Number:	
Confirmation Number:	7419
Title of Invention:	Semiconductor memory device testable with a single data rate and/or dual data rate pattern in a merged data input/output pin test mode
First Named Inventor/Applicant Name:	Jong-Cheol Lee
Customer Number:	22150
Filer:	Frank Chau/marg dougan
Filer Authorized By:	Frank Chau
Attorney Docket Number:	8836-223JHM/ID12244US
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	071007OA-AMENDMENT_8 836-223.pdf	356009 733cd3a09b40a05149223a5e7219b 12ab942	no	8

Warnings:

Information:	Total Files Size (in bytes):	356009
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